

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Complete if Known Substitute for form 1449B/PTO 10/708,222 Application Number **INFORMATION DISCLOSURE** 02/17/2004 Filing Date STATEMENT BY APPLICANT First Named Inventor Ming-Dou Ker 2816 **Group Art Unit** ZWEIZ19 (use as many sheets as necessary) **Examiner Name** of Attorney Docket Number | SISP0009USA Sheet

		OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JZ	1	H. NEUTÈBOOM, B.M. J. KUP, M. JANSSENS; A DSP-based hearing instrument IC; IEEE Journal of Solid-State Circuits, Nov. 1997; page 1790 - page 1806; VOL. 32.	
JZ	2	H. BANBA, H. SHIGA, A. UMEZAWA, T. MIYABA, T. TANZAWA, S. ATSUMI, K. SAKUI; A CMOS bandgap reference circuit with sub1-V operation; IEEE Journal of Solid-State Circuits; May 1999; page 670 - page 674; VOL. 34	
JZ	3	G. GIUSTOLISI; A Low-Voltage Low-Power Voltage Reference Based on Subthreshold MOSFETs; IEEE Journal of Solid-State Circuits; Jan. 2003, page 151 - page 154; VOL.38, No.1	
JZ	4	P. MALCOVATI, F. MALOBERTI, M. PRUZZI, C. FIOCCHI; Curvature compensated BiCMOS bandgap with 1-V supply voltage; IEEE Journal of Solid-State Circuits; July 2001; page 52 - page55; VOL.36, No.7	
JZ	5	YUEMING JIANG, EDWARD K.F. LEE; A 1.2 V bandgap reference based on transimpedance amplifier; ISCAS 2000-IEEE international symposium on Circuits and Systems; May 23-31, 2000; page 261 - page 264	
, JZ	6	SHU-YUAN CHIN, CHUNG-YU WU, A New Type of Curvature-Compensated CMOS Bandgap Voltage Reference; VLSITSA; 1991; page 398 - page 402	
JZ	7	KA NANG LEUNG, IEEE, PHILIP K. T. MOK; A Sub-1-V 15-ppm/-C CMOS Bandgap Voltage Reference without Requiring Low Threshold Voltage Device; IEEE Journal of Solid-State Circuits; April 2002; page 526 - page 530; VOL.37, No.4	
JZ	8	S.M.SZE; Physics of Semiconductor Devices; John Wiley & Sons. Inc.; 1985; page 28-29; 2en ed.	
		•	
		•	

Examiner Jeffrey Zweizig	Date Considered	9/26/05
--------------------------	--------------------	---------

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.